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Zeev ZALEVSKY, et al.

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GROUP
2874

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
<i>KSW</i>	1	5,960,133	28 Sep '99	TOMLINSON, W. J.	<i>385</i>	<i>18</i>	
<i>KSW</i>	2	5,987,040	16 Nov '99	NESSET, D. et al.	<i>372</i>	<i>8</i>	

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
<i>KSW</i>	3	WO 97/09536	13 Mar '97	PCT				
<i>KSW</i>	4	GB 2 191 302	09 Dec '87	United Kingdom	<i>G02F</i>	<i>3/00</i>		

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>KSW</i>	5	URUSHIDANI, S. et al.; "A High-Performance Switch Architecture for Free-Space Photonic Switching Systems;" IEICE Transactions on Communications; Vol. E82-B; No. 2; February 1999; pp. 298-305; XP000940180
<i>KSW</i>	6	MASASHI, H. et al.; "Programmable Optical Parallel Processor Based Upon Polarization Modulation: Cascade Operations;" Applied Optics; Vol. 28; No. 20; 15 October 1989; pp. 4305-4312; XP000071267

EXAMINER

Kevin S. Wood

DATE CONSIDERED

5/5/03

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.